

SK7102 FlexPoint™ PS/2 Pointing Stick Mouse Encoder Sprintek Corporation

FEATURES

- Low cost and space saving solution
- Built-in oscillator, analog and digital circuit
- Low count of total components. Only several resistors and capacitor are needed
- Advanced motion control algorithm
- Z-Tap algorithm to simulate left button click, double click and drag
- CellMute™ technology filters the wireless EMI noise from cellular phones and wireless networks
- Works with standard Windows® mouse drivers
- Works with Lenovo TrackPoint® drivers
- Low profile QFN 24 pin package: 4x4mm 0.60 Max (LxWxH)
- Low power consumption. 0.89 uA(low power mode), 2.76 mA (operation mode)
- 3.0V to 5.25V operating voltage
- Industry temperature range: -40°C to +85°C

APPLICATION

- Notebooks/Laptops
- Netbooks
- Handhelds
- Keyboards
- Instrumentation

ORDERING INFORMATION

SK7102-LT QFN 24-pin, 0.5mm pitch, (4x4mm 0.6 MAX), Pb-Free, RoHS compliant

DESCRIPTION

The SK7102 FlexPoint™ pointing stick mouse encoder is a cost-effective, space-saving and highly integrated IC that interfaces a force-stick sensor to PS/2 port.

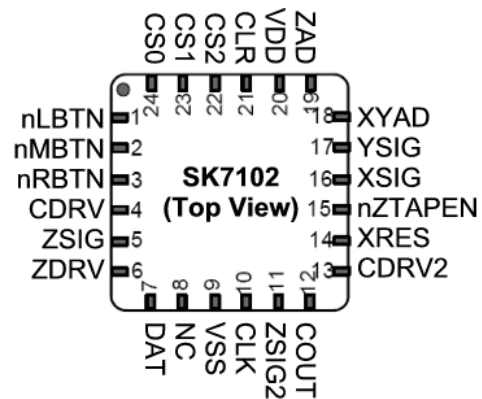
The SK7102 works with most of popular force stick sensors with tolerance up to +/- 20%. The SK7102 highly integrates oscillator, analog and digital circuit. Only several low tolerance external components are needed.

Typically, consuming 0.89 uA, the SK7102 is ideal for battery-operated systems.

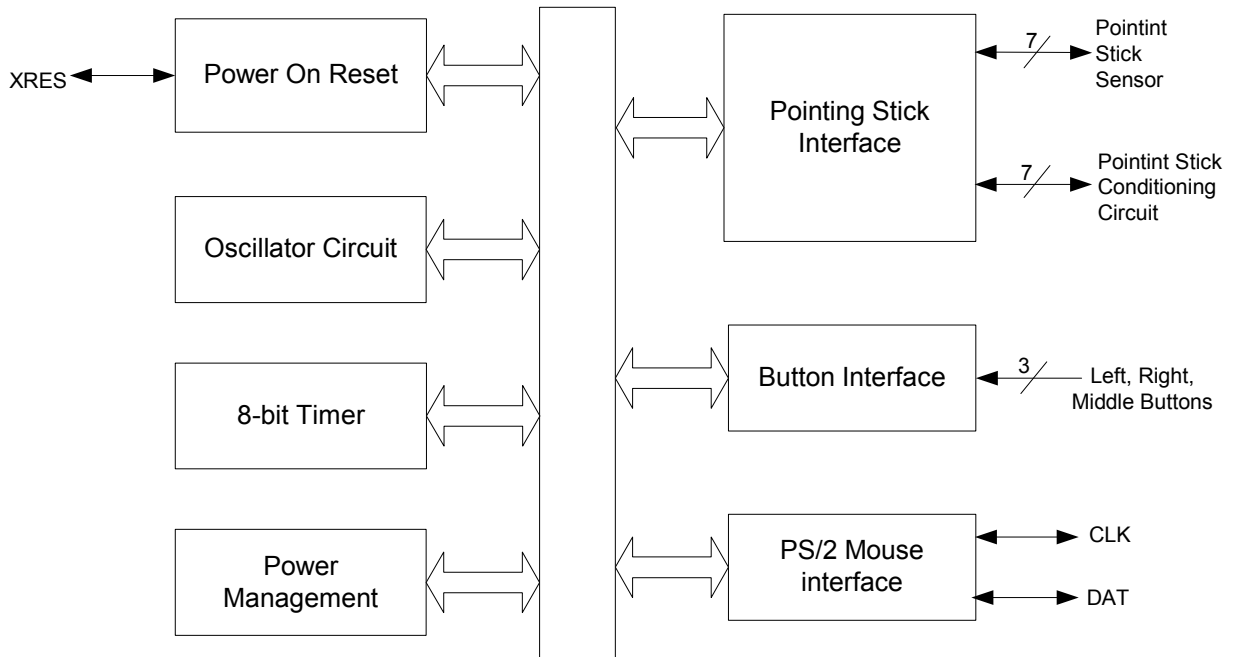
Deploying CellMute™ technology to filter the wireless EMI noise from cellular phones and wireless networks, the SK7102 can work quietly in wireless environment.

The SK7102 partially implements the Lenovo TrackPoint® extended command protocol and can work with Lenovo TrackPoint® device drivers directly.

PIN ASSIGNMENTS



FUNCTION BLOCK DIAGRAM



PIN DEFINITION

Pin No	Type	Name	Description
1	IO	nLBTN	Left button: active low with internal pull-up resistor.
2	IO	nMBTN	Middle button: active low with internal pull-up resistor.
3	IO	nRBTN	Right button: active low with internal pull-up resistor.
4	IO, A	CDRV	Sensor common drive
5	IO, A	ZSIG	Sensor Z signal input
6	IO, A	ZDRV	Sensor Z drive
7	IO	DAT	PS/2 mouse data
8	IO	NC	Not used
9	P	VSS	Ground connection
10	IO	CLK	PS/2 mouse clock
11	IO, A	ZSIG2	Second sensor Z signal input
12	O	COUT	Signal conditioning circuit output pin
13	IO, A	CDRV2	Second sensor common drive
14	I	XRES	Active high external reset with internal pull down.
15	IO	nZTAPEN	Z-Tap function enable pin: tied to ground for enable, leave floating for use internal flash setting
16	IO, A	XSIG	Sensor X signal input
17	IO, A	YSIG	Sensor Y signal input
18	IO, A	XYAD	Signal conditioning circuit control pin
19	IO, A	ZAD	Signal conditioning circuit control pin
20	P	VDD	Power supply
21	IO, A	CLR	Signal conditioning circuit control pin
22	IO, A	CS2	Signal conditioning circuit control pin
23	IO, A	CS1	Signal conditioning circuit control pin
24	IO, A	CS0	Signal conditioning circuit control pin

LENGENG P = Power, A = Analog, I = Input, O = Output, IO = Input/Output

The center pad on the QFN package should be connected to ground (VSS) for best mechanical, thermal, and electrical performance. If not connected to ground, it should be electrically floated and not connected to any other signal.

FUNCTION BLOCK DESCRIPTION

The SK7102 consists functionally of several major sections (see the block diagram on the previous page). These include the pointing stick interface, the button interface, the PS/2 mouse interface, the power on reset, the oscillator circuit, the 8-bit timer and power management. All sections communicate with each other and operate concurrently.

Pointing Stick Interface

The SK7102 interfaces with a sensor that has four strain gauges – one on each of the sides. If force is applied precisely in the X direction, it produces a change of resistance of only the two X gauges. Force in the Y direction causes changes only in the Y gauges. One of the pair of gauges increases, while the other decreases in resistance. If the user presses on the sensor from the top (Z axis), all of the strain gauges increases in resistance. When the test current is passed through the gauges, these resistance changes are converted into voltage changes.

The signal conditioning circuit balances and amplifies the incoming signals for digitizing by a built-in A/D converter. Sprintek's circuit can correct a significant imbalance between the gauges in each X and Y pair ($\pm 20\%$), allowing relaxed manufacturing tolerances for the sensors and cabling.

The SK7102 implements Z-Tap feature. The feature allows users to tap on pointing stick to simulate left button click, double click and drag functions. The feature can be enabled by connecting JMP1; otherwise the feature will depend on the flash setting that is disabled at default.

Quick tap – left button click (select)

Quick tap twice – left button double click (execute)

Press and hold – left button down (Start drag); Release – left button up (Release drag)

Quick release – left button click (select). The feature is enabled by driver only.

Button Interface

The SK7102 scans the button status along with the pointing stick sensor sampling.

PS/2 Mouse Interface

The SK7102 has internal 5.6K pull-up resistors on data and clock lines. The SK7102 follows IBM standard PS/2 mouse protocol to communicate with the host. The SK7102 transforms the digitized X, Y data into mouse report format and send it to the host. The SK7102 command protocol is described in the PS/2 communication section in this document.

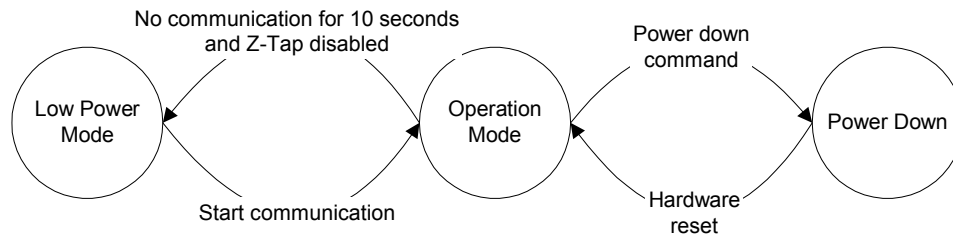
Power Management

The SK7102 has three power modes: operation mode, low power mode and power down mode. The SK7102 automatically switches the power modes to minimize the power consumption.

When the SK7102 runs in operation mode, the power consumption is about TBD mA in this mode when there is communication;

When there is not communication between the SK7102 and host for 10 seconds, and Z-Tap is disabled, the SK7102 switches to low power mode. In this mode, the SK7102 samples at rate of 1/3 of operation mode. The power consumption is about TBD μ A in this mode.

When the SK7102 receives power down command "E2, 44", the SK7102 enters power down mode. The only way to put the SK7102 out of from power down mode is a hardware reset. The power consumption is about 3 μ A in this mode.



Power Mode State Transition Diagram

Power On Reset Circuit

The SK7102 has build-in low voltage detector. The reset threshold is set to 2.92V.

Oscillator Circuit

The SK7102 has build-in oscillator circuit and no external crystal or resonator is needed. The SK7102 includes operation oscillator circuit and sleep oscillator circuit. The operation circuit provides the clock to all internal circuit. The sleep oscillator circuit provides clock to 8-bit timer to wakeup the SK7102 from sleep mode.

8-bit Timer

The 8-bit timer provides the timing control for PS/2 communication, pointing stick sampling, sleep timer wakeup.

PS/2 COMMUNICATION

Power on Reset

At start-up or reset, the SK7102 waits for about 650 milliseconds, and then sends x"AA" to the host, followed by a device ID of x"00". Then the IC sets itself to its default values, i.e. incremental stream mode with 1:1 scaling, and a sampling rate of 100 Hz. The IC then disables itself until a command x"F4" is received from the host.

PS/2 Data Report Format

The list shows the PS/2 report formats. In the data report, each of the two position values (X and Y) is expressed as a 9-bit two's complement integer with the most significant bit (the sign bit) stored separately in byte 1. If there is an overflow of the accumulator, the maximum positive or negative count is reported. The overflow bits are always set to 0.

Byte 1	b0	Left Button status	1 = depressed
	b1	Right Button Status	1 = depressed
	b2	Middle Button Status	1 = depressed
	b3	Reserved Always = 1	
	b4	X8: MSB of X data, sign bit	1 = negative
	b5	Y8: MSB of Y data, sign bit	1 = negative
	b6	Always 0	
	b7	Always 0	
Byte 2	b0	X0: LSB of X data	
	b1	X1	
	b2	X2	
	b3	X3	
	b4	X4	
	b5	X5	
	b6	X6	
	b7	X7	
Byte 3	b0	Y0: LSB of Y data	
	b1	Y1	
	b2	Y2	
	b3	Y3	
	b4	Y4	
	b5	Y5	
	b6	Y6	
	b7	Y7	

PS/2 Error Handling

The SK7102 sends an acknowledge x"FA" for every correct command or parameter received from the host.

The SK7102 issues a Resend request x"FE" after the receipt of an invalid input. If a second invalid input is received after the Resend request, an error code x"FC" is transmitted to the host. Resend and error codes are sent for both commands and parameters. A response from the SK7102 is transmitted within 25 milliseconds. If the SK7102 is in stream mode, the host must disable the SK7102 before issuing a command that requires a response from the SK7102.

Standard PS/2 Commands

The SK7102 implements all standard PS/2 mouse commands.

Reset (x"FF")

The system will issue a "Reset" command to initiate a software reset. The SK7102 controller will set a sampling rate of 100 reports per second, resolution 02, stream mode, disabled, scaling off. The SK7102 controller response will be x"FA" followed by the two byte packet x"AA 00".

Resend (x"FE")

The system can send this command when it detects an error in any transmission from the SK7102 controller. The "Resend" command should be sent following a SK7102 controller packet transmission and before the system enables the interface allowing the next SK7102 controller output. On receipt of "Resend", the SK7102 controller retransmits the previous packet, of one, two, or three bytes as the case may be. The ACK byte x"FA" is not counted in determining the size of a packet unless it was the only response, in which case the x"FA" will be resent. If a "Resend" command is received by the SK7102 controller from the host immediately following a three byte data packet transmission from the SK7102 controller to the host, the SK7102 controller should resend the three byte data packet.

Set Default (x"F6")

Following receipt of this command, the SK7102 controller will set a sampling rate of 100 reports per second, resolution 02, stream mode, disabled, scaling off. The Accumulators and button status bits will be cleared. The SK7102 controller response will be x"FA".

Disable (x"F5")

When in stream mode, this command disables the initiation of transmissions by the SK7102 controller. While disabled, the SK7102 controller will respond to all other commands. If the SK7102 controller is in stream mode, the host should disable it prior to sending any command that requires a response from the SK7102 controller. When in remote mode, this command has no effect, but will be reflected in the status byte as described in "Status Request" (x"E9"), and will be effective on return to Stream mode. The SK7102 controller response will be x"FA".

Enable (x"F4")

This command allows the SK7102 controller to begin transmissions to the host if it is in stream mode at the current sampling rate. When in remote mode, this command has no effect, but will be stored for later use when in stream mode and will be reflected in the status byte as described in "Status Request" (x"E9"). The SK7102 controller response will be x"FA".

Set Sampling Rate (x"F3,XX")

This command is effective only stream mode. When in remote mode, this command has no effect, but will be stored for later use when in stream mode and in the status byte as described in "Status Request" (x"E9"). A rate of 100 reports per second is the reset default. This is a two byte command where the second byte indicates the sampling rate. The valid values are x"0A", x"14", x"28", x"3C", x"50", x"64", x"C8". The SK7102 controller response will be x"FA".

The actual sampling rate of SK7102 is always 100 per second. The sensor measurement rate is 100 per second, to maintain proper performance.

Read Device Type (x"F2")

The SK7102 controller will always respond with an x"00" to this command, corresponding to a "mouse-like" device.

Set Remote Mode (x"F0")

This command resets stream mode. In remote mode, data values are reported only in response to a "Read Data" command. The SK7102 controller response will be x"FA".

Set Wrap Mode (x"EE")

This command places the SK7102 controller in wrap mode. After transmission of the ACK byte, all data bytes except x"FF" or x"EC" that are sent to the SK7102 controller will be immediately returned by the

SK7102 controller (without ACK). The SK7102 controller will remain in the wrap mode until it receives a "Reset Wrap Mode" (x"EC") or "Reset" (x"FF") command.

Reset Wrap Mode (x"EC")

This command resets wrap mode. The SK7102 controller will return to the previous mode of operation after receiving this command. However, if "Set Wrap Mode" is entered after the SK7102 controller has been operating in "Stream Mode", the SK7102 controller will reenter "Stream Mode" disabled after receipt of a "Reset Wrap Mode" command. If the SK7102 controller is not in the wrap mode when this command is received, the command will be acknowledged, but its state will not be affected. The SK7102 controller response will be x"FA".

Read Data (x"EB")

This command requests that the SK7102 controller transmit all the data defined in the data packet format as described in "Data Report". This command may be executed in either remote or stream mode. The data will be transmitted even if there has been no activity since the last report. The integer portion of the count accumulators will be cleared. The SK7102 controller response will be x"FA" followed by a three byte packet with the data format as specified in "Data Report".

Set Stream Mode (x"EA")

This command sets the SK7102 controller in stream mode. This command resets remote mode. If enabled, data will be transmitted to the system in the format described in "Data Report" when there is any activity detected. The SK7102 controller response will be x"FA".

Status Request (x"E9")

When this command is issued by the host, the SK7102 controller will respond with a 3-byte status report as follows:

Byte 1	b0	Right Button Status	1 = depressed
	b1	Middle Button Status	1 = depressed
	b2	Left Button status	1 = depressed
	b3	Reserved Always = 0	
	b4	Scaling	0 = 1:1; 1 = 2:1
	b5	Enable/Disable	0 = Disable; 1 = Enable
	b6	Stream/Remote mode	0 = Stream; 1 = Remote
	b7	Always 0	
Byte 2	Current resolution		
Byte 3	Current sampling rate		

Set Resolution (x"E8,XX")

This command has no effect for the SK7102 controller. The default value is x"02". The SK7102 controller response will be x"FA". Valid values are x"00", x"01", x"02", x"03".

Set Scaling 2:1 (x"E7")

This command has no effect in the SK7102 controller. The SK7102 controller response will be x"FA".

Reset Scaling 1:1 (x"E6")

This command has no effect in the SK7102 controller. The SK7102 controller response will be x"FA".

Read Secondary ID (x"E1")

This command will read the secondary device ID of the pointing device (2 bytes). The least significant byte is sent first. The SK7102 controller response will be x"01, 0E".

Extended PS/2 Commands (x"E2")

The SK7102 extends PS/2 mouse commands to support pointing stick specific feature control using command x"E2".

Power Down (x"E2, 44")

Power down the controller permanently. The only recovery from this command is a hardware reset.

Power On Reset (x"E2, 7F")

Following the acknowledgment to this command, the controller will proceed to execute instructions from its power on or hard reset vector. All parameters will be reset to their power on default values, and a POST completion response of x"AA 00" will be transmitted upon completion.

Read ROM Version (x"E2, 46")

The command is to read ROM version. The SK7102 controller response will be x"3E".

Read Extended Button Status (x"E2, 4B")

This command will return a single byte containing information about the number of hardware buttons available, and the number of buttons that are presented to the host. The format will consist of two 4 bit nibbles, the most significant 4 bits (bits 7-4) will contain the number of hardware buttons present in the controller, and the lower 4 bits (bits 3-0) will contain the number of buttons that may be presented to the host. The four valid responses will be x"22", x"23", x"32", x"33". The SK7102 controller response will be x"33".

Force Calibration (x"E2, 51")

This command will perform the whole pointing stick calibration cycle after the acknowledge byte is sent. The cycle will take approximately 600 milliseconds to complete. The SK7102 controller response will be x"FA".

The following commands are used to manipulate the SK7102 internal registers. Refer to the SK7102 internal register table following this section.

Quick Read Register (x"E2, XX")

This command is to read the contents of register "XX". The second byte is from x"00" to x"3F".

Read Register (x"E2, 80, XX")

This command is to read the contents of register "XX". The command is identical to the Quick Read Register command x"E2 XX" for arguments < x"3F".

Write Register (x"E2, 81, XX, YY")

This command is to write byte "YY" to register "XX".

Read Register Block (x"E2, 82, XX")

This command is to read the contents of register "XX" to "XX + 5". The response has 6 bytes and the content in low location is returned first.

Get Register Page Number (x"E2, 83")

This command is to return current register page number. The default page number is 0. The page number will be reset to 0 when the SK7102 receives commands Reset (x"FF") and Set Default (x"F6").

Set Register Page Number (x"E2, 84, XX")

This command is to set current register page to page "XX".

Toggle Register Bits (x"E2, 47, XX, YY")

This command is to toggle specific register bits. Byte three "XX" is the address of the register, in the range of x"00"-x"FF". Byte four is logically exclusive ORed with the current register value.

Internal Register Table in Page 0

Register Index	Bit Position	Description	Default
2C			b00x0000x
	0	Z-Tap enable. The bit is set by hardware configuration at default.	
	1	Unused	
	2	Unused	
	3	Invert X axis	
	4	Invert Y axis	
	5	Invert Z axis. The bit is set by hardware configuration at default.	
	6	Exchange X axis and Y axis	
	7	Unused	
42		0 = map Z-Tap state to left button; others = map Z-Tap state to right button.	00
4A		X and Y output sensitivity factor.	80
58		Drag control	00
	0	1 = drag enable; 0 = drag disable	
	1-7	unused	
5A		Release to select enable. 0xFF = disable; 0x00 = enable.	00
5E		Z time constant	38
90 - FF		SK7102 control and operation registers	

Internal Register Table in Page 1

The page 1 is configuration page. All register read operations will get data from non-volatile memory. All register write operation write data to RAM. Reading register x"C0" in this page leads to load non-volatile memory to RAM; Write register x"C0" in this page leads to write RAM copy to non-volatile memory. After writing register x"C0", a Power on Reset (x"E2, 7F") is required to make new configuration registers (x"06" to x"3F") take effect.

Register Index	Bit Position	Description	Default
00 – 01		Part number	71 02
02		Version	
03		Revision	
04		Manufacture product ID	00
05		Reserved	00
06 – 3F		Configuration registers	
C0		Register read operation = Read x"00" – x"3F" to RAM; Always return x"C0" when read. Register write operation = Write RAM copy to non-volatile memory. The write operation for this register takes about 250ms.	
Others		Reserved	

APPLICATION NOTES

Power Circuit

The reference design uses On Semiconductor's LDO NCP699 and Torex Semiconductor's LDO XC6204B to keep power noise low. You may choose low noise LDOs from any other manufacturers. Do not share LDO circuit with other circuits to get best performance.

Adjust X, Y, Z Sampling Sensitivity

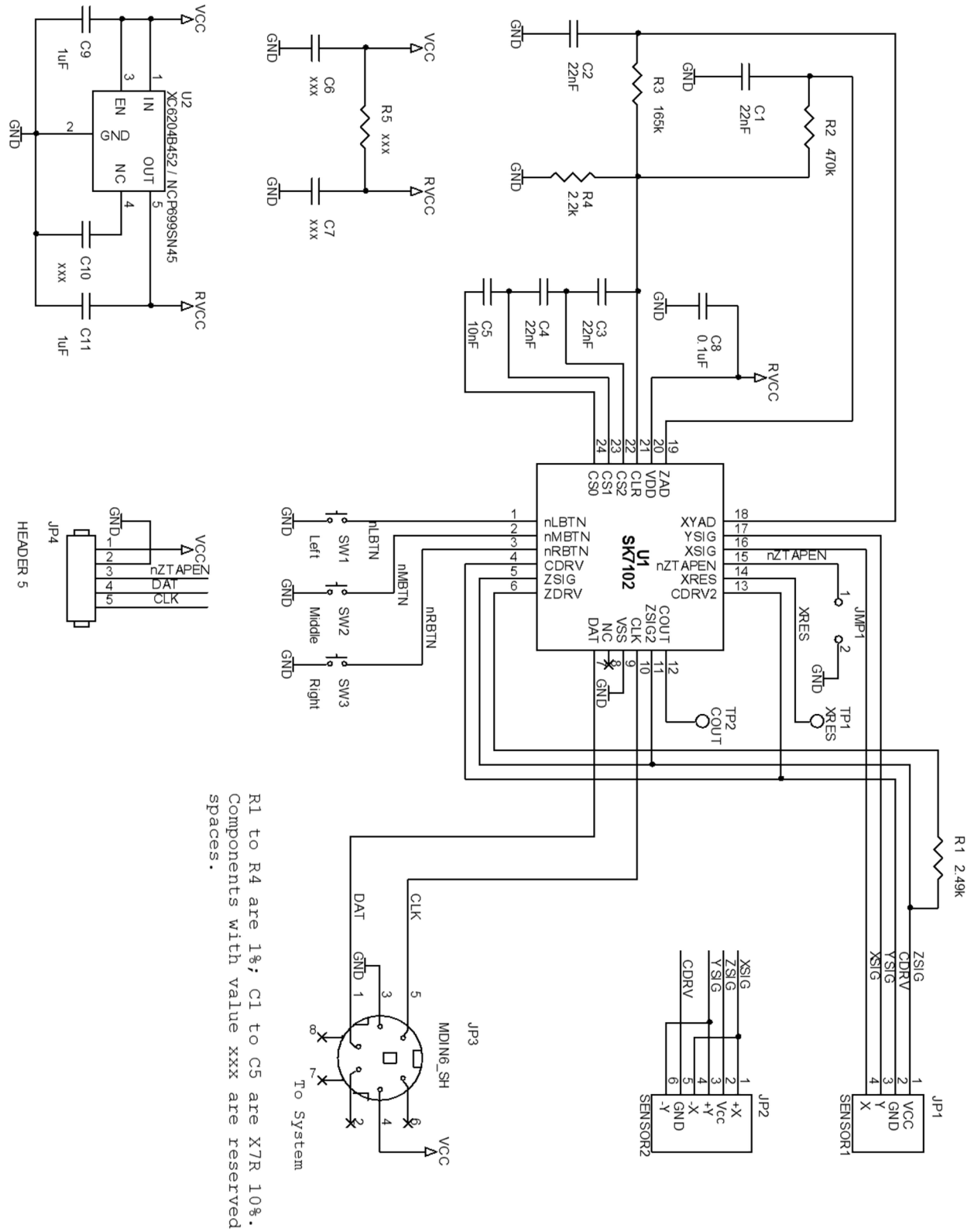
The sampling signal inputs in X, Y and Z can be adjusted through change hardware components R2, R3. Increasing R2 causes bigger signal output in X and Y axis; increasing R3 causes bigger signal output in Z axis.

C1 to C5 shall be X7R 10% or better and R1 to R4 shall be 1% to reduce the module signal output variance.

Use Different Sensors

The reference design uses a 2.5K ohm sensor. You may use different sensors from any pointing stick manufacturers. Change R1 to the closest value of sensor resistor value; and then follow "adjust X, Y, Z sampling sensitivity" to adjust X, Y and Z outputs.

SCHEMATIC OF REFERENCE DESIGN



R1 to R4 are 1%; C1 to C5 are X7R 10%. Components with value xxx are reserved spaces.

BOM OF REFERENCE DESIGN

Item	Qty	Reference	Description	Manufacturer	Mfr Part Number *
1	4	C1, C2, C3, C4	CAP 22000pF 50V CERAMIC X7R 10% 1206	GENERIC	311-1176-1-ND
2	1	C5	CAP 10000PF 50V CERAMIC X7R 10%1206	GENERIC	311-1174-1-ND
3	1	C8	CAP .10UF 50V CERAMIC X7R 10% 1206	GENERIC	311-1179-1-ND
4	2	C9, C11	CAP 1.0uF 25V CERAMIC X7R 10% 1206	GENERIC	495-1909-1-ND
5	3	C6, C7, C10	DO NOT POPULATE. May be used for other power circuits.	N/A	N/A
6	1	JMP1	HDR 2 POS 0.1"	N/A	N/A
7	1	JP1	POINTING STICK SENSOR 1	**	N/A
8	1	JP2	POINTING STICK SENSOR 2	**	N/A
9	1	JP3	CONN MINI DIN RECEPT RT ANG 6POS	GENERIC	275-1043-ND
10	1	JP4	HDR 5 POS 0.1"	N/A	N/A
11	1	R1	RES 2.49K OHM 1/4W 1% 1206 SMD	GENERIC	RHM2.49KFCT-ND
12	1	R2	RES 470K OHM 1/4W 1% 1206 SMD	GENERIC	RHM470KFCT-ND
13	1	R3	RES 165K OHM 1/4W 1% 1206 SMD	GENERIC	RHM165KFCT-ND
14	1	R4	RES 2.2K OHM 1/4W 1% 1206 SMD	GENERIC	RHM2.20KFCT-ND
15	1	R5	DO NOT POPULATE. May be used for other power circuits.	N/A	N/A
16	3	SW1, SW2, SW3	SWITCH TACT 6MM SQ 100GF MOM	GENERIC	SW263CT-ND
17	2	TP1, TP2	THRUHOLE TEST POINT	N/A	N/A
18	1	U1	POINTING STICK CONTROLLER, QFN-24	SPRINTEK	SK7102-LF
19	1	U2	LOW NOISE, LOW DROPOUT 4.5V LDO REGULATOR 2%	TOREX or On Semiconductor	XC6204B452MR or NCP699SN45

* If the manufacturer is "GENERIC", the "Mfr Part Number" is Digi-Key part number.

** Please contact us for the complete pointing stick sensor manufacturer list.

ELECTRONICS SPECIFICATION**Absolute Maximum Ratings**

Symbol	Description	Min	Typ	Max	Units	Notes
TSTG	Storage Temperature	-55	-	+100	°C	
VDD	Supply Voltage on Relative to VSS	-0.5	-	+6.0	V	
VIO	DC Input Voltage	VSS-0.5	-	VDD+0.5	V	
VIOZ	DC Voltage Applied to Tri-State	VSS-0.5	-	VDD+0.5	V	
IMIO	Maximum Current into any Port Pin	-25	-	+50	mA	
ESD	Electro Static Discharge Voltage	2000	-	-	V	Human Body Model ESD
LU	Latch-up Current	-	-	200	mA	

Operating Temperature

Symbol	Description	Min	Typ	Max	Units	Notes
TOP	Operating Temperature	-40	-	+85	°C	

DC Electrical Characteristics

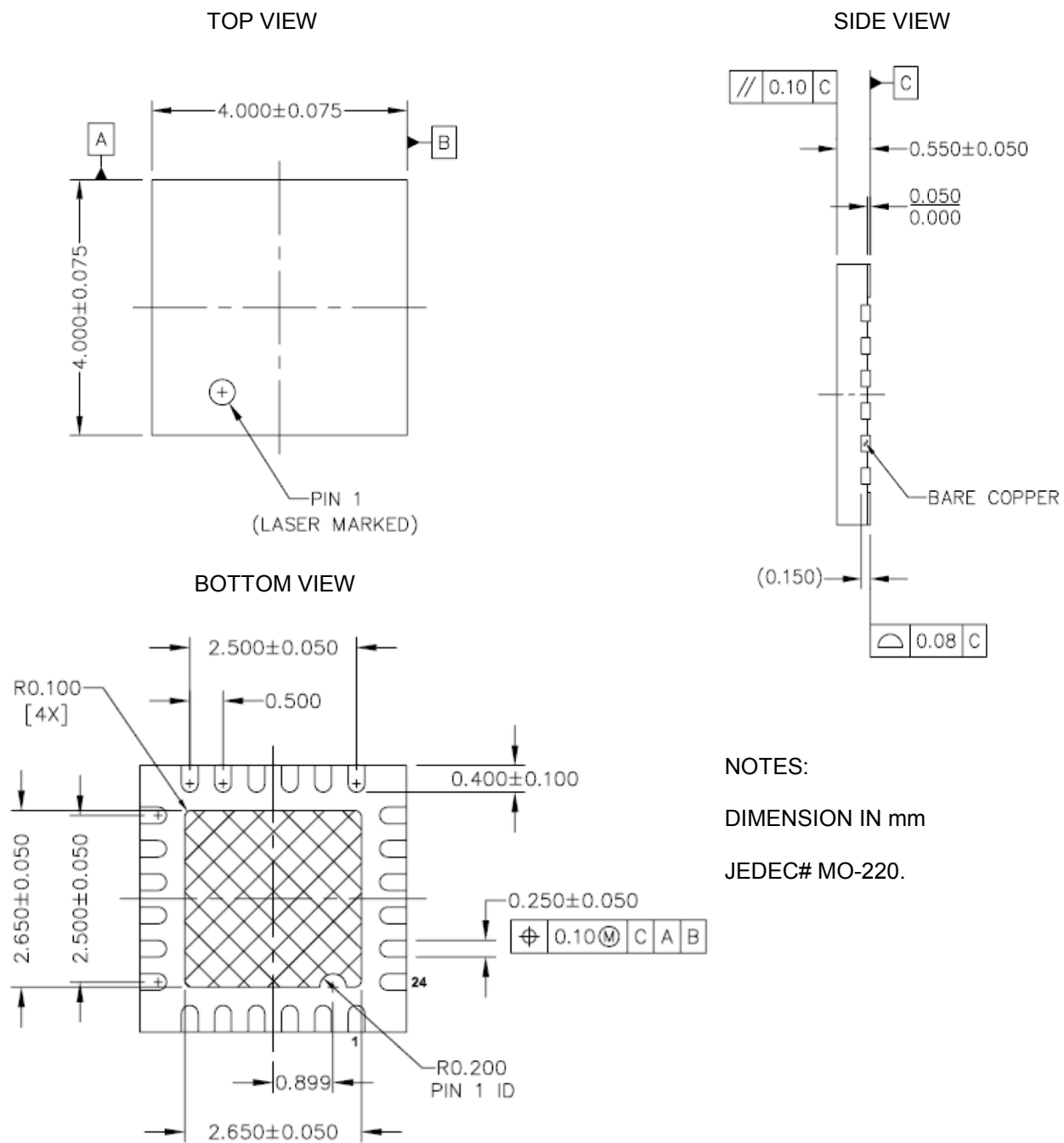
Symbol	Description	Min	Typ	Max	Units	Notes
VDD	Supply Voltage	3.0	-	+5.25	V	
IDD	Supply Current when IC is in operation mode		2.76		mA	
IDD2	Supply Current when IC is in low power mode		0.89		mA	
ISD	Supply Current when IC is in power down mode		32		uA	Including typical LDO current 27uA
RPU	Pull-up Resistor	4	5.6	8	kΩ	
VOH	High Output Level	VDD-1.0	-	-	V	
VOL	Low Output Level	-	-	0.75	V	
VIL	Input Low Level	-	-	0.8	V	
VIH	Input High Level	2.0	-	-	V	
LVD	Low voltage Detection	2.75	2.92	2.99	V	

Sensor Characteristics

Symbol	Description	Min	Typ	Max	Units	Notes
RS	Each Sensor Resistor Value	0.3	-	4	kΩ	
R1/R2	Matching Ration in X and Y direction	80	-	120	%	
TRS	Tolerance of Each Sensor Resistor	-	-	20	%	

PACKAGING INFORMATION

SK7102-LT Drawing



SK7102-LT 24-Lead (4x4mm 0.60 MAX) QFN

SALE AND SERVICE INFORMATION

To obtain information about Sprintek Corporation or pointing stick encoder family sales and technical support, reference the following information.

Sprintek Corporation

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Simi Valley, CA 93063, USA

Web Site: <http://www.sprintek.com>

REVISION HISTORY

Revision	Issue Date	Description
1.00	3/1/2009	Initial Preliminary Release
1.01	4/18/2016	Updated power consumption data